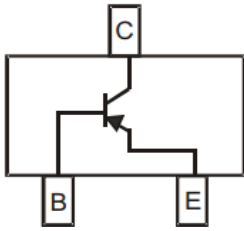


PNP General Purpose Amplifier



SOT-323

Features

- Epoxy meets UL-94 V-0 flammability rating and halogen free
- Moisture Sensitivity Level 1
- High Conductance
- Part no. with suffix "Q" means AEC-Q101 qualified

Applications

- Switching and linear amplification

Mechanical Data

- **Case:** SOT-323
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** K3F

■ Maximum Ratings (Ta=25°C unless otherwise noted)

Item	Symbol	Unit	Value
Collector-Base Voltage	V_{CBO}	V	-60
Collector-Emitter Voltage	V_{CEO}	V	-60
Emitter-Base Voltage	V_{EBO}	V	-5
Collector Current -Continuous	I_C	mA	-600
Total Device Dissipation (*)	P_D	mW	200
Thermal Resistance Junction to Ambient (*)	R_{thJA}	K/W	625
Junction Temperature	T_j	°C	-55 to +150
Storage Temperature	T_{STG}	°C	-55 to +150

(*) Device mounted on FR-4 PCB 1.0 x 1.0 x 0.06 inch



MMST2907AQ

RoHS
COMPLIANT

■ Electrical Characteristics (Ta=25°C unless otherwise noted)

Item	Symbol	Unit	Conditions	Min	Max
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	V	$I_C = -10mA, I_B = 0$	-60	
Collector-base breakdown voltage	$V_{(BR)CBO}$	V	$I_C = -10\mu A, I_E = 0$	-60	
Emitter-base breakdown voltage	$V_{(BR)EBO}$	V	$I_E = -10\mu A, I_C = 0$	-5	
Collector cut-off current	I_{CEX}	nA	$V_{CE} = -30V, V_{EB} = -0.5V$		-50
Collector cut-off current	I_{CBO}	nA	$V_{CB} = -50V, I_E = 0$		-20
DC current gain	h_{FE}		$V_{CE} = -10V, I_C = -0.1mA$	75	
	h_{FE}		$V_{CE} = -10V, I_C = -1mA$	100	
	h_{FE}		$V_{CE} = -10V, I_C = -10mA$	100	
	h_{FE}		$V_{CE} = -10V, I_C = -150mA$	100	300
	h_{FE}		$V_{CE} = -10V, I_C = -500mA$	50	
Collector-emitter saturation voltage	$V_{CE(sat)}$	V	$I_C = -150mA, I_B = -15mA$		-0.4
			$I_C = -500mA, I_B = -50mA$		-1.6
Base-emitter saturation voltage	$V_{BE(sat)}$	V	$I_C = -150mA, I_B = -15mA$		-1.3
			$I_C = -500mA, I_B = -50mA$		-2.6
Transition frequency	f_T	MHz	$V_{CE} = -20V, I_C = -50mA, f = 100MHz$	200	
Delay time	t_d	ns	$V_{CC} = -30V,$ $I_C = -150mA, I_{B1} = -15mA$		10
Rise time	t_r	ns			40
Storage time	t_s	ns	$V_{CC} = -6V,$ $I_C = -150mA, I_{B1} = I_{B2} = -15mA$		225
Fall time	t_f	ns			60

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
MMST2907AQ	F2	Approximate 0.006	3000	30000	120000	7" reel



■ Characteristics (Typical)

Fig.1 - Static characteristic

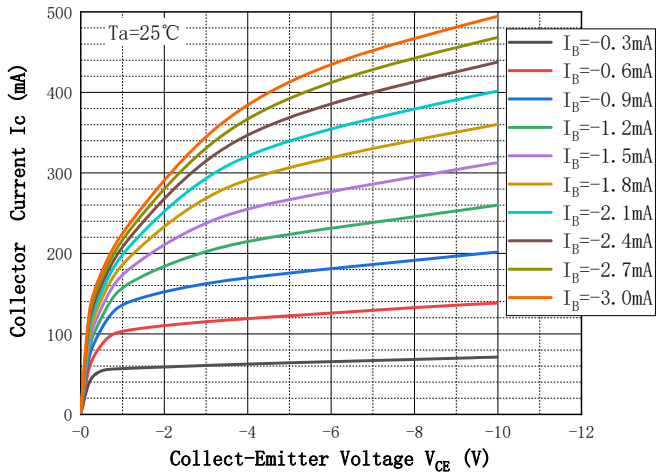


Fig.2 - DC Current Gain

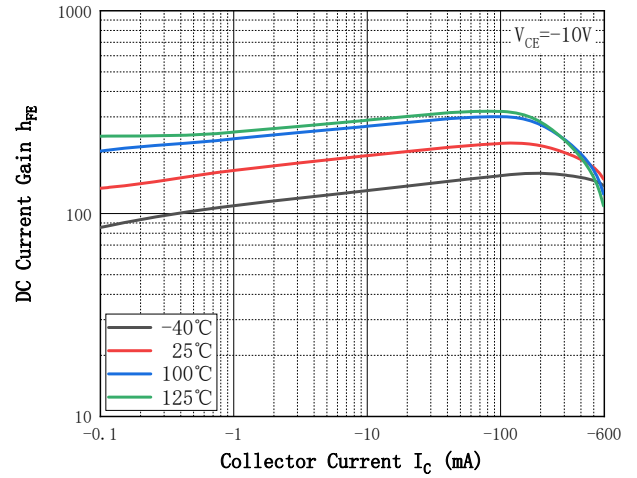


Fig.3 - Collect-Emmitter Saturation Voltage

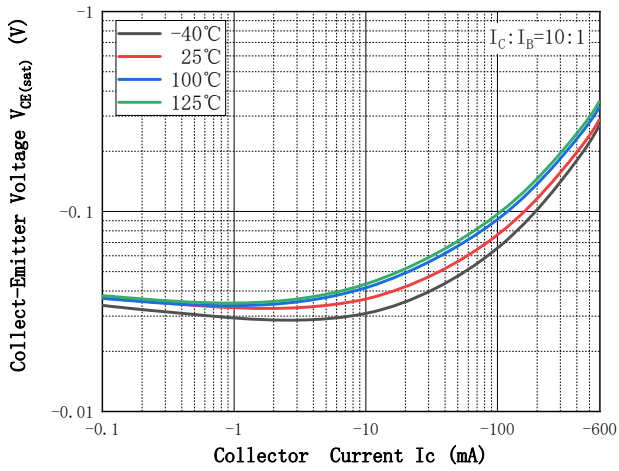


Fig.4 - Base-Emmitter Voltage

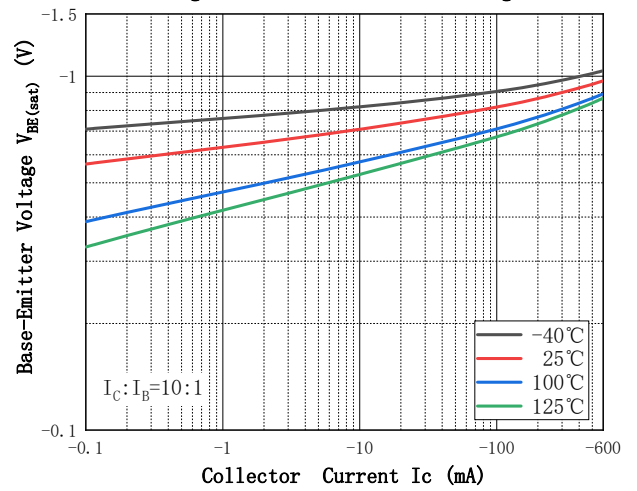


Fig.5 - Base-Emmitter On Voltage

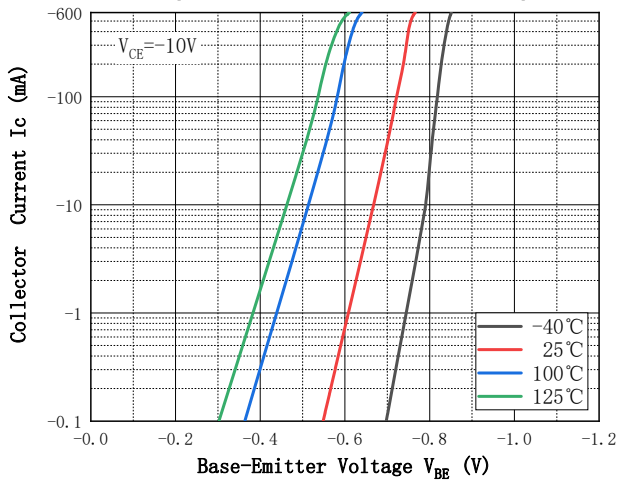
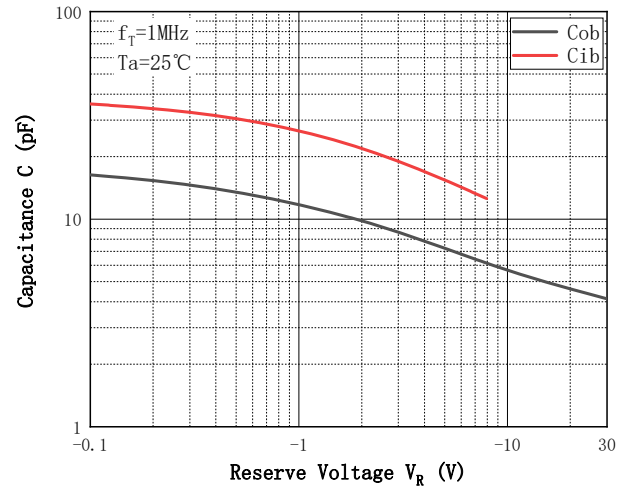
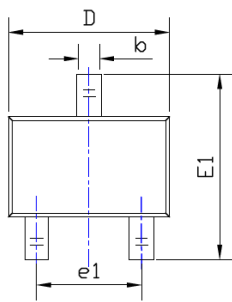


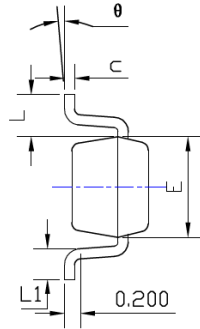
Fig.6 - Cob/Cib—VCB/VEB



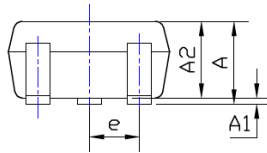
■SOT-323 Package Outline Dimensions & Suggested Pad Layout



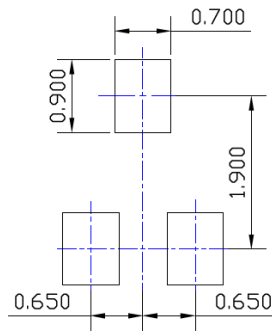
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT: mm

SUGGESTED SOLDER PAD LAYOUT

SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.016	0.150	0.400
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.096	2.150	2.450
e	0.026TYP		0.650TYP	
e1	0.047	0.055	1.200	1.400
L	0.021REF		0.525REF	
L1	0.010	0.018	0.260	0.460
theta	0°	8°	0°	8°

NOTE:

- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
- 3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.



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